

Delve into the World of Post-Silicon Validation and Debug with Prabhat Mishra's Comprehensive Guide

In the rapidly evolving world of integrated circuits (ICs), post-silicon validation and debug have become increasingly critical steps in the design process. These tasks ensure that the fabricated chip meets its specifications and functions flawlessly in real-world conditions. For engineers and designers working on complex IC designs, having a comprehensive understanding of post-silicon validation and debug is essential. Prabhat Mishra's book, "Post-Silicon Validation and Debug," provides a comprehensive guide to this essential aspect of IC design.

"Post-Silicon Validation and Debug" is a comprehensive reference book that covers all aspects of post-silicon validation and debug. The book is divided into three main parts:

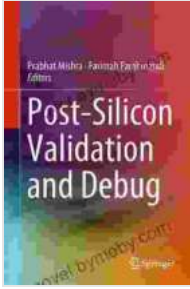
1. **Fundamentals of Post-Silicon Validation:** This part provides a solid foundation in the principles and techniques of post-silicon validation. It covers topics such as test planning, test generation, and fault simulation.
2. **Advanced Techniques for Post-Silicon Debug:** This part dives into advanced debugging techniques, including scan chain debug, clock domain crossing debug, and memory debug. It also discusses the use of emulation and simulation for debug purposes.
3. **Real-World Case Studies:** This part presents real-world case studies that demonstrate the application of post-silicon validation and debug

techniques in practical scenarios. These case studies provide valuable insights into the challenges and solutions encountered in industry.

- Provides a comprehensive overview of post-silicon validation and debug techniques
- Covers both fundamental and advanced topics in depth
- Presents real-world case studies that demonstrate the practical application of these techniques
- Written by an experienced industry expert with over 20 years of experience in post-silicon validation and debug
- Includes numerous illustrations, examples, and exercises to aid comprehension
- Gain a solid understanding of the fundamentals of post-silicon validation and debug
- Learn advanced debugging techniques to resolve complex design issues
- Apply best practices to improve the efficiency and effectiveness of post-silicon validation and debug processes
- Get insights into industry-proven solutions through real-world case studies
- Enhance your knowledge and skills in this critical area of IC design

"Post-Silicon Validation and Debug" is tailored to the following audience:

Post-Silicon Validation and Debug by Prabhat Mishra



★★★★★ 5 out of 5

Language : English
File size : 22638 KB
Text-to-Speech : Enabled
Screen Reader : Supported
Enhanced typesetting : Enabled
Print length : 649 pages

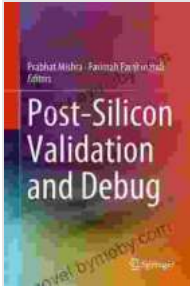


- IC design engineers and designers
- Verification and validation engineers
- Test engineers
- Debug engineers
- Project managers involved in IC design projects
- Researchers and students in the field of IC design

Prabhat Mishra is a seasoned industry veteran with over 20 years of experience in post-silicon validation and debug. He has held various leadership positions at major semiconductor companies, including Intel, Marvell, and Qualcomm. Mishra is also an active contributor to industry forums and conferences, and he has authored numerous technical papers on post-silicon validation and debug.

"Post-Silicon Validation and Debug" by Prabhat Mishra is an invaluable resource for engineers and designers working on complex IC designs. Its comprehensive coverage of both fundamental and advanced topics, coupled with real-world case studies, makes it a must-have for anyone involved in post-silicon validation and debug. By leveraging the knowledge

and techniques presented in this book, engineers can significantly improve the efficiency and effectiveness of their post-silicon validation and debug processes, ensuring the successful delivery of high-quality IC products.



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